If you are using a printed copy of this procedure, and not the on-screen version, then you <u>MUST</u> make sure the dates at the bottom of the printed copy and the on-screen version match.

The on-screen version of the Collider-Accelerator Department Procedure is the Official Version.

Hard copies of all signed, official, C-A Operating Procedures are kept on file in the C-A ESHQ

Training Office, Bldg. 911A.

### C-A OPERATIONS PROCEDURES MANUAL

### **ATTACHMENT**

### 4.120.4.f 4 O'Clock (PEER 9) Chipmunk Tests from MCR

C-A-OPM Procedures in which this Attachment is used.			
4.120.4			

# **Hand Processed Changes**

HPC No.	<u>Date</u>	Page Nos.	<u>Initials</u>	
		Signature on File ollider-Accelerator Departn	nent Chairman	 Date

V. Castillo

# 4.120.4.f 4 O'Clock (PEER 9) Chipmunk Tests from MCR

### PASS ANNUAL ACCEPTANCE TEST PROTOCOL

Division A Software Filename and Checksum: Title:	Checksum:	
Division B Software Filename and Checksum: Title:	Checksum:	
<u>Initial testing complete</u> :		
Test Team Leader's Name (Print):	Life Number:	
Test Team Leader's Name (Sign):	Date:/	
Acceptance test procedure complete (following repairs and retesting if required):		
Test Team Leader's Name (Print):	_ Life Number:	
Test Team Leader's Name (Sign):	Date:/	
<u>Test results reviewed by</u> :		
Safety Section Head's Name (Print):	Life Number:	
Safety Section Head's Name (Sign):	Date://	
Test results accepted by Radiation Safety Committee:		
RSC Member's Name (Print):	Life Number:	
NOC MEMBER STAIRE (LIMI).	Life Number:	
RSC Member's Name (Sign):	Date:/	

Do Divisions in series 1.1 **TEST** Chipmunk C250 located at 4GI1 - RF in MODE 2 **□** VERIFY Chipmunk calibration date [ \_\_\_\_/\_\_\_] is **VALID** □ VERIFY Chipmunk is in good physical condition and is **CHIRPING** □ VERIFY MCR sees Chipmunk: Div A □ and Div B □ as O.K. **DETACH** Cable from Chipmunk □ VERIFY MCR sees Chipmunk as TRIP & FAILSAFE Attempt to reset Trip & Failsafe at MCR □ VERIFY **FAIL** Chipmunk **Test box** to the **Chipmunk cable CONNECT** RESET Interlock & Failsafe at MCR **MCR** sees Chipmunk: **Div**  $A \square$  and **Div**  $B \square$  as O.K. □ VERIFY **PRESS** A Div Trip button on the test box □ VERIFY MCR sees the Chipmunk Div A as **RAD** B Div Trip button on the test box **PRESS** MCR sees the Chipmunk Div B as □ VERIFY **RAD** RESET Rad Interlock at MCR □ VERIFY MCR sees C250 Div A □ and Div B □ are OK **PRESS** A Div Fail button on the test box MCR sees the Chipmunk Div A as **FAILSAFE** □ VERIFY **PRESS** B Div Fail button on the test box □ VERIFY MCR sees the Chipmunk Div B as **FAILSAFE** RESET Failsafe at MCR □ VERIFY MCR sees C250 Div A  $\square$  and Div B  $\square$  are OK **DETACH** Chipmunk Test box from cable **CONNECT** Chipmunk to cable **Chipmunk** is **CHIRPING** □ VERIFY RESET Interlock & Failsafe at MCR □ VERIFY O.K. MCR sees Chipmunk: Div A  $\square$  and Div B  $\square$  as Check for acceptance of Test Chipmunk C250 in Mode 2 1.2 **TEST** Chipmunk C251 located at 1005S 4GE3 labyrinth in MODE 2 **□** VERIFY Chipmunk calibration date [\_\_\_\_/\_\_\_] is **VALID** Chipmunk is in good physical condition and is □ VERIFY **CHIRPING** MCR sees Chipmunk: Div A □ and Div B □ as □ VERIFY O.K. **DETACH** Cable from Chipmunk TRIP & FAILSAFE MCR sees Chipmunk as □ VERIFY □ VERIFY Attempt to reset Trip & Failsafe at MCR **FAIL** Chipmunk Test box to the Chipmunk cable CONNECT RESET Interlock & Failsafe at MCR □ VERIFY MCR sees Chipmunk: Div A □ and Div B □ as O.K. **PRESS** A Div Trip button on the test box MCR sees the Chipmunk Div A as **RAD** □ VERIFY **PRESS B** Div Trip button on the test box MCR sees the Chipmunk Div B as **RAD** □ VERIFY RESET Rad Interlock at MCR MCR sees C251 Div A □ and Div B □ are OK □ VERIFY

PRESS	A DIV Fall button on the test box	
VERIFY	MCR sees the Chipmunk Div A as	FAILSAFE
PRESS	B Div Fail button on the test box	
VERIFY	MCR sees the Chipmunk Div B as	FAILSAFE
		O.V.
VERIFY	MCR sees C251 Div A $\square$ and Div B $\square$ are	OK
DETACH	Chipmunk <b>Test box</b> from cable	
	<u>-</u>	CHIRPING
VERIFY	MCR sees Chipmunk: Div A $\square$ and Div B $\square$ as	O.K.
□ Check f	for acceptance of Test Chipmunk C251 in Mode 2	
TEST	Chipmunk C252 located at 1004A – 4GE2 in MODE 2	
VERIFY	Chipmunk calibration date [/ ] is	VALID
VERIFY	Chipmunk is in good physical condition and is	CHIRPING
VERIFY	<b>MCR</b> sees Chipmunk: <b>Div A</b> $\square$ and <b>Div B</b> $\square$ as	O.K.
DETACH	Cable from Chipmunk	
VERIFY	•	TRIP & FAILSAFE
VERIFY		FAIL
CONNECT	<u> </u>	
RESET	Interlock & Failsafe at MCR	
	<b>MCR</b> sees Chipmunk: <b>Div</b> A $\square$ and <b>Div</b> B $\square$ as	O.K.
	-	
		RAD
	*	RAD
VERIFY	MCR sees C252 Div A $\square$ and Div B $\square$ are	OK
PRESS	A Div Fail button on the test box	
VERIFY	MCR sees the Chipmunk Div A as	FAILSAFE
PRESS	B Div Fail button on the test box	
VERIFY	MCR sees the Chipmunk Div B as	FAILSAFE
RESET	Failsafe at MCR	
	MCR sees C252 Div A □ and Div B □ are	RESET
VERIFY		
VERIFY DETACH	Chipmunk <b>Test box</b> from cable	
DETACH CONNECT	Chipmunk to cable	
DETACH CONNECT VERIFY	Chipmunk to cable Chipmunk is	CHIRPING
DETACH CONNECT	Chipmunk to cable	CHIRPING O.K.
	CONNECT VERIFY RESET VERIFY  Check for the connect of the connect	DETACH CONNECT Chipmunk Test box from cable CONNECT Chipmunk is RESET Interlock & Failsafe at MCR VERIFY MCR sees Chipmunk: Div A □ and Div B □ as  Check for acceptance of Test Chipmunk C251 in Mode 2  TEST Chipmunk C252 located at 1004A − 4GE2 in MODE 2  VERIFY Chipmunk calibration date [ / ] is VERIFY Chipmunk is in good physical condition and is VERIFY MCR sees Chipmunk: Div A □ and Div B □ as DETACH Cable from Chipmunk VERIFY MCR sees Chipmunk as VERIFY MCR sees Chipmunk as VERIFY Attempt to reset Trip & Failsafe at MCR CONNECT Chipmunk Test box to the Chipmunk cable RESET Interlock & Failsafe at MCR VERIFY MCR sees Chipmunk: Div A □ and Div B □ as PRESS A Div Trip button on the test box VERIFY MCR sees the Chipmunk Div A as PRESS B Div Trip button on the test box VERIFY MCR sees the Chipmunk Div A as RESET Rad Interlock at MCR VERIFY MCR sees C252 Div A □ and Div B □ are  PRESS A Div Fail button on the test box VERIFY MCR sees C252 Div A □ and Div B □ are

## END OF TEST PROCEDURE

TTL: Sign for completion of initial testing:		-
	Date://	_
TTL: Sign for completion of final testing:		_
	Date: / /	